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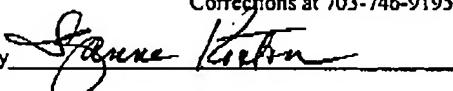
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Re: U.S. Application No.: 10/683,632
Filing Date: October 10, 2003
Applicants: Yang et al.
Title: Precision Low Jitter Oscillator Circuit
Attorney Docket No.: 764164-605086

Dear Sir or Madam:

An error has been noted on the attached Filing Receipt. Please correct the Atty. Docket No. from "764164600007" to - 764164605086 -.

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CLI-1109161v1

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10/683,632	10/10/2003	2817	1180	764164605097 086	5	38	1

CONFIRMATION NO. 1196

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FILING RECEIPT



"OC000000011675870"

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Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Frederick Edward Sykes, Burlington, CANADA;

Domestic Priority data as claimed by applicant

This application is a CON of 09/833,376 04/12/2001 PAT 6,633,202

Foreign Applications

If Required, Foreign Filing License Granted: 01/12/2004

Projected Publication Date: 04/22/2004

Non-Publication Request: No

Early Publication Request: No

Title

Precision low jitter oscillator circuit

Preliminary Class